

Ben-Gurion University of the Negev

Faculty of Engineering Science

School of Electrical and Computer Engineering

Dept. of Electrical and Computer Engineering

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| Error Correction Encoder & Decoder | **Digital Design and Logical Synthesis for Electrical Computer Engineering**  **(36113611)**  **Course Project** |
| **Digital High Level Design**  **Version 0.1** |

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**Revision Log**

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| **Rev** | **Change** | **Description** | **Reason for change** | **Done By** | **Date** |
| 0.1 | Initial document |  |  |  |  |
| 0.2 |  |  |  |  |  |
| 0.3 |  |  |  |  |  |
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# Blocks Functional Descriptions

<<This section should state the functional description of each element in the design. This will include: (1) input-process-output of each block (stated in detail in sub-sections under Section 2 herein), (2) signal/interface definitions (entities/architectures, functions, procedures, components, etc., (3) model hierarchy and diagram of what you’ve implemented (attach the details as a figure, if possible). Any key algorithms and data structures—such as for the FIFO blocks—should be described in this section as well. >>

### << component/feature #1 name>>

<<Edit the titles of these subsections with your specific names>>

Example:**Text

Description automatically generated with medium confidence**>>

1. view of the block.

Example:

Table

Description automatically generated

1. Block interface.

### << component/feature #2 name>>

<< Simply copy/paste the Heading 2 line and rename it if you need more subsections that what is here. >>

### << component/feature #3 name>>

<< Delete the Heading 2 lines you don’t need. >>

### << component/feature #4 name>>

<< For any figures, number them sequentially, and copy/paste them into the document. Use Hdl Designer OLE drag and drop for drawing diagrams instead of Word’s drawing tools.. >>

# Appendix

## Terminology

**LSB** - Least Significant Bit

**TBR** - To Be Reviewed

**TBD** - To Be Defined

## References

<<I expect your team’s work to make appropriate citing of relevant literature—whether it is product manuals, the text, or other works you have read or consulted in order to do the project. If you don’t have any references, then leave the section blank. >>